

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLN. OF: ISHIKAWA  
SERIAL NO.: 10/714,768  
FILED: November 17, 2003  
FOR: Delay Producing Method, Delay Adjusting Method Based on the Same...  
DOCKET: ELPIDA WN-2632

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Dear Sir:

In connection with the above-entitled matter, Applicant hereby proffers U.S. Patent Office Form PTO-1449, including copies of the references listed therein. Where available, English language counterparts and/or abstracts also are provided for Examiner's convenience. These references are discussed in the specification. The claims in the present application are believed to be patentably distinguished over these references.

This Information Disclosure Statement is being made pursuant to the duty of disclosure imposed by law and formulated in 37 CFR 1.56(A). No representation is made that the information thus disclosed in fact constitutes prior art or that it is the closest prior art, inasmuch as 37 CFR 1.56(A) relies on a materiality concept which depends on subjectivity.


The enclosed Information Disclosure Statement is being submitted prior to issuance of the first action on the merits. Therefore, we believe there are no fees involved with this Information Disclosure Statement. In the event there are any fee deficiencies or additional fees

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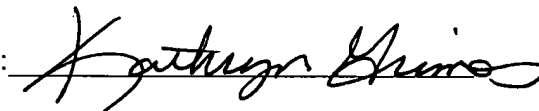
are payable, please charge them (or credit any overpayment) to our Deposit Account No. 08-1391.

Respectfully submitted,

  
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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 2, 2003 at Tucson, Arizona.

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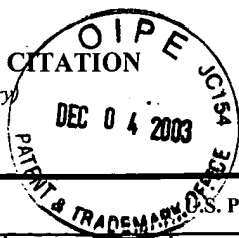
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# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

ELPIDA WN-2632

Application Number

10/714,768

Applicant(s)

ISHIKAWA

Filing Date

November 17, 2003

Group Art Unit

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		6,369,627	4.9.02	TOMITA	327	158	

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		2001-111394	4.20.01	JAPAN (w/abstract)	H03K	5/135		
		2001-056723	2.27.01	JAPAN (w/abstract)	G06F	1/10		

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		"A 1-Gb/s/pin 512-Mb DDRII SDRAM Using a Digital DLL and a Slew-Rate-Controlled Output Butter" Matano et al., Article, 3 pgs.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and no considered. Include copy of this form with next communication to applicant.